VSC7718, VSC7728

GPON and GE-PON OLT Transimpedance Amplifier and Limiting Amplifier Chipset

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FEATURES:	BENEFITS:
▸ Burst average received power monitor	▸ Enables the system to monitor the burst power to determine if there are any issues with the ONU transmission
▸ Reset through the high-speed data interface	▸ Reduces pin count for the BOSA
▸ Fast settling time	▸ Enables the receiver to meet GPON settling-time requirements
▸ Differential Data Ready (DATRDYP, DATRDYN) output	▸ Used to latch the receive power monitor as well as tell the downstream CDR when to start acquiring good data
▸ LVPECL outputs	▸ Standard interface that directly couples to the CDR
▸ Bare die or 16-pin 3 mm x 3 mm QFP package	▸ Small enough to fit in a SFF or SFP module along with a laser driver to achieve a complete solution

APPLICATIONS:

▸ GPON - G.984 1244 Mbps upstream, class A, B, B+, and C

▸ GE-PON - IEEE Std. 802.3ah (EFM), 1000BASE-PX10/20-U (GE-PON OLT)

SPECIFICATIONS:

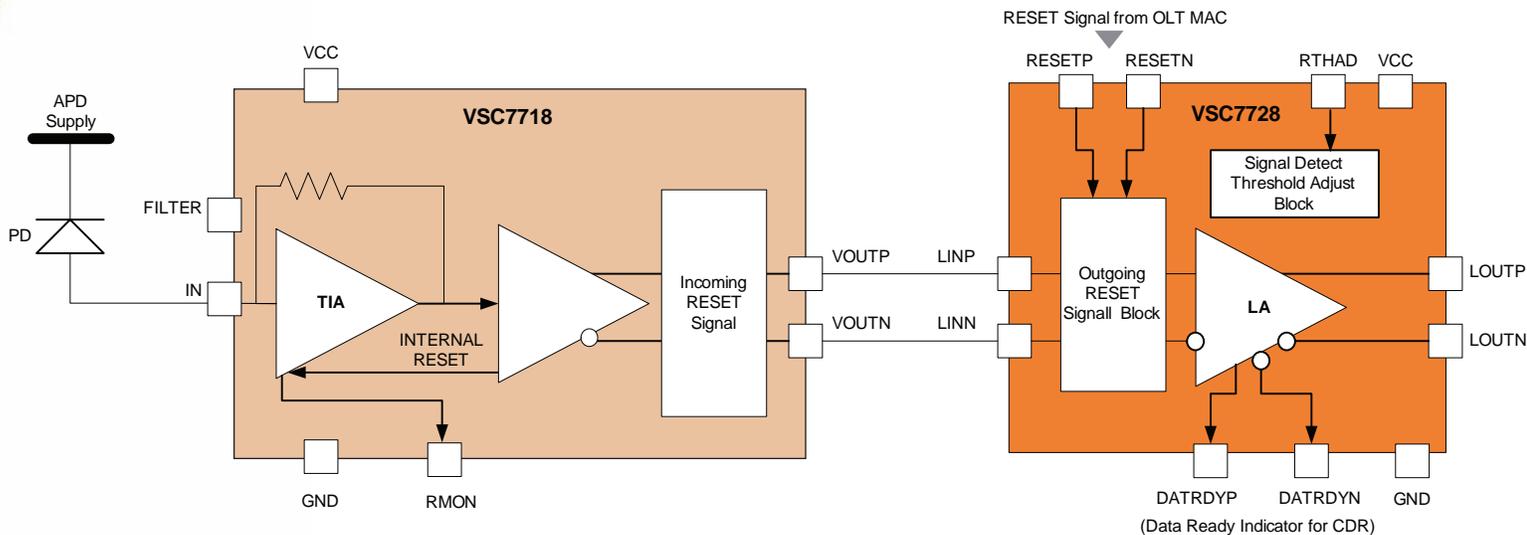▸ 3.3 V power supply

GENERAL DESCRIPTION:

The VSC7718 transimpedance amplifier (TIA) and VSC7728 post amplifier together form a complete chipset solution for Gigabit-capable Passive Optical Networks (GPON) and Gigabit Ethernet Passive Optical Networks (GE-PON) OLT burst-mode receivers. This chipset exceeds GPON requirements with better than –33 dBm sensitivity, industry-leading overload performance of better than –4 dBm and over 23 dBm of dynamic range.

Designed to be used in conjunction with one another, the VSC7728 supplies a reset signal to the VSC7718 in order to control its internal offset correction circuitry, providing fast settling times with immunity to long streams of consecutive identical digits. The VSC7718 also features a space-saving filter connection provided for positive bias to a PIN diode through a filter resistor to VCC.

The VSC7718 is available as bare die with padout optimized for standard 4-pin, 5-pin and 6-pin TO-46 packages. The VSC7728 is available in a 16-pin, 3mm x 3mm QFP package.

BLOCK DIAGRAM: